

Docket No.: GR 00 P 4121

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Albert Birner et al.
Appl. No. : 09/871,013
Filed : May 31, 2001
Title : Method for Making Electrical Contact with a Rear Side of a
Semiconductor Substrate during its Processing
Art Unit :

ASSOCIATE POWER OF ATTORNEY

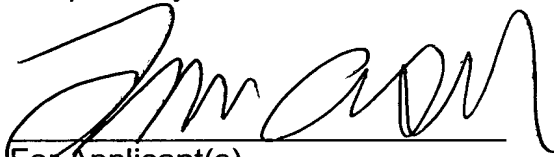
Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Please recognize GREGORY L. MAYBACK (Reg. No. 40,719) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted,


For Applicant(s)

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Date: July 17, 2001

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